Please add the following new claims:

CL

73.   (New) The media processing apparatus of Claim 72,

wherein the processor includes

an instruction memory for storing a plurality of task programs that show contents of the input/output processing,

wherein the processor switches between and executes the plurality of task programs.

74.   (New) The media processing apparatus of Claim 73,

wherein the processor further includes:

a program counter unit having a plurality of program counters that correspond to the plurality of task programs, respectively;

an instruction fetch unit that fetches instructions one by one from the instruction memory in accordance with instruction addresses indicated by one of the plurality of program counters;

an instruction execution unit that executes the fetched instructions one by one; and

a task control unit that switches from a currently executed task program to another by instructing the instruction fetch unit to fetch instructions in accordance with instruction addresses indicated by another program counter.

75.   (New) The media processing apparatus of Claim 74,

wherein the task control unit switches from the currently executed task program to another when a predetermined number of instruction cycles, which is determined differently for each task program, passes, one instruction cycle corresponding to fetching and execution of one instruction.